REMARKS

This amendment is in response to the Office Action dated February 7, 2005. Reconsideration of the above-identified application in view of the amendments above and the following remarks is respectfully requested.

Claims 1-46 are currently pending in the application. Claims 1-46 have been rejected. Claims 1, 11, 12, 20, 33, 43, and 44 have been amended.

Claims Objections

Claims 1, 11, 12, 20, 33, 43, and 44 are hereby amended in accordance with the Examiner's objections. The claims are believed to now be in an appropriate format for acceptance.

Claims Rejections Under 35 USC 102

Claims 1-46 are rejected under 35 U.S.C. 102(a) as being anticipated in view of the citation "Asynchronous Gate-Diffusion-Input (GDI) Circuits".

In reviewing the Examiner's comments it is found that the proper publication date for the reference "Asynchronous Gate-Diffusion-Input (GDI) Circuits" is August 2004, which date is shown at the top of the front page of the citation. Due to a typographical error, which was made in the IDS, the publication date of the cited reference was errouneously given as May 2002, and the applicant apologizes for the confusion caused.

For clarity, a list of publications by Arkadiy Morgenshtein is provided below:

[1] A. Morgenshtein, M. Moreinis and R. Ginosar, "Asynchronous Gate-Diffusion-Input (GDI) Circuits", *IEEE Transactions on Very Large Scale Integration Systems*, vol. 12, no. 8, pp. 847- 856, August 2004.

[2] A. Morgenshtein, A. Fish, I.A. Wagner, "Gate-Diffusion Input (GDI) – A Power Efficient Method for Digital Combinatorial Circuits", *IEEE Transactions on Very Large Scale Integration Systems*, vol. 10, no. 5, pp. 566-581, October 2002.

[3] A. Morgenshtein, A. Fish, I.A. Wagner, "An Efficient Implementation of D-Flip-Flop Using The GDI Technique", *Proc. of ISCAS'04 Conference*, Canada, pp. 673-676, May 2004.

[4] A. Morgenshtein, A. Fish, I.A. Wagner, "Gate-Diffusion Input (GDI) – A Technique for Low Power Design of Digital Circuits: Analysis and Characterization", *Proc. of ISCAS'02 Conference*, vol. 1, pp. 477-480, USA, May 2002.

[5] A. Morgenshtein, A. Fish, I.A. Wagner, "Gate-Diffusion Input (GDI) – A Novel Power Efficient Method for Digital Circuits: A Detailed Methodology", *Proc. of 14th IEEE International ASIC/SOC Conference*, pp. 39-43, USA, Sept. 2001.

The Applicant notes that the current application was filed in August 2004. The Applicant therefore asserts that the cited reference does not constitute ground for rejection of the current claims under 35 USC 102(a).

It is believed that all of the matters raised by the Examiner are overcome, and that all of the claims are both novel and inventive.

In view of the foregoing, it is believed this application is now in condition for allowance. An early Notice of Allowance is respectfully requested.

Respectfully submitted,

Sol Sheinbein
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Date: April 11, 2005